



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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11/14/2  
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In re application of: Salvatore N. Storino, et al. : Date: November 1, 2002  
Group Art Unit: 2823 : IBM Corporation  
Examiner: K. Nguyen : Intellectual Property Law  
Serial No.: 09/751,163 : Dept. 917, Bldg. 006-1  
Filed: December 29, 2000 : 3605 Highway 52 North  
Title: METHOD FOR ELIMINATION OF : Rochester, MN 55901  
PARASITIC BIPOLAR ACTION IN  
SILICON ON INSULATOR (SOI)  
DYNAMIC LOGIC CIRCUITS

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

**CERTIFICATE OF MAILING  
UNDER 37 C.F.R. 1.8(a)**

I hereby certify that this correspondence  
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envelope addressed to: Assistant  
Commissioner for Patents, Washington,  
D.C. 20231, on November 1, 2002.

*Lisa M Plank*

Lisa M. Plank

AMENDMENT

This Amendment addresses the issues raised in the Office Action mailed  
September 11, 2002, having a period for response set to expire on December 11, 2002. The  
following amendments and remarks are respectfully submitted as a full and complete response.

11/07/2002 CCHAU1 00000136 090465 09751163  
01 FC:1202 18.00 CH

Docket No.: RO998-200B  
Serial No.: 09/751,163

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